

Amendments to the Specification

Please replace the Title with:

TRANSACTION SWITCH AND INFINIBAND HYBRID CHANNEL
ADAPTER/SWITCH INCORPORATING SAME

Please replace the paragraph beginning at line 12 on page 21 with the following amended paragraph:

Coupled to one of the PCI buses 216 is a local CPU 208, for programming the hybrid channel adapter/switch 202 and I/O controllers 206. Coupled to the CPU 208 is a local memory 218 for storing programs and data for the CPU 208. In addition, the local memory 218 may be used to store data from the I/O controllers 206. For example, if the hybrid channel adapter/switch 202 is included in an I/O unit 108 that is a RAID controller performing RAID 5 operations, it may be desirable to buffer the data coming from the disks coupled to the I/O controllers 206 in the local memory 218 in order to perform the necessary exclusive OR operations. Among other things, the hybrid channel adapter/switch 202 is capable of performing Direct RDMA operations as described in co-pending U.S. Patent Number 6,594,712 ~~Application~~ entitled INFINIBAND CHANNEL ADAPTER FOR PERFORMING DIRECT DMA BETWEEN PCI BUS AND INFINIBAND LINK, ~~filed October 17, 2000~~, which is hereby incorporated by reference.

Please replace the paragraph beginning at line 5 on page 37 with the following amended paragraph:

In one embodiment, the buffer manager 414 allocates the shared buffer memory 304 substantially in accordance with the invention described in co-pending U.S. Patent Application serial number 09/740694 entitled METHOD AND APPARATUS FOR OVER-ADVERTISING INFINIBAND BUFFERING RESOURCES (BAN:0104), filed December 19, 2000, which is hereby incorporated by reference.